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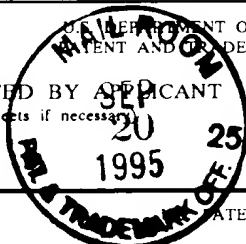
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Brent Keeth et al.

FILING DATE

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PATENT DOCUMENTS

*Examiner Initial		Document Number	Date	Name	Class	Subclass	Filing Date If Appropriate
	AA						
	AB						
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	AK						

FOREIGN PATENT DOCUMENTS

		Document Number	Date	Country	Class	Subclass	Translation	
							Yes	No
	AL							
	AM							
	AN							
	AO							
	AP							

OTHER PRIOR ART (including Author, Title, Date, Pertinent Pages, Etc.)

AR		Sugibayashi, T. et al., "A 30ns 256-Mb DRAM With A Multidivided Array Structure", IEEE Journal of Solid-State Circuits, Vol. 28, No. 11, November 1993, pp. 1092-1097
AS		Kitsukawa et al., "256-Mb DRAM Circuit Technologies for File Applications", IEEE Journal of Solid-State Circuits, Vol. 28, No. 11, November 1993, pp. 1105-1113
AT		

EXAMINER

DATE CONSIDERED